



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant Gary Dan Dotson

Title: PROGRAMMABLE ERROR  
CHECKING VALUE CIRCUIT AND  
METHOD

Appl. No.: 09/675,855

Filing Date: 9/29/2000

Examiner: (not yet assigned)

Art Unit: (not yet assigned)

Atty. Dkt. No. 00AB147 (81696730) 04 CO  
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Technology Center 2100

**CERTIFICATE OF MAILING**  
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on the date below.

Susan D. Wishaw

(Printed Name)

(Signature)

November 10, 2000  
(Date of Deposit)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.97-98 and in accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits herewith the references listed on the attached Form PTO-1449 to the Examiner such that they may be considered and made of record in the above-identified application. A copy of each reference is enclosed.

Respectfully submitted,

Date November 9, 2000

By Alexander M. Gerasimow

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